



*Approved  
-EWS*

*#7*

501.20699VC1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: R. HORI  
Serial No.: 07/458,507  
Filed: December 28, 1989  
For: SEMICONDUCTOR INTEGRATED CIRCUIT  
Art Unit: 236  
Examiner: S. Baker

*DEC 07 1989*

PROPOSED DRAWING CORRECTIONS

Honorable Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

November 27, 1990

Sir:

It is proposed that the drawings in the above-identified application be amended in accordance with the attached red-lined print. Approval of the same is respectfully requested at this time.

Upon receipt of the approval of the amendment to the drawings and receipt of the official Notice of Allowance, the drawing amendments will be effected in accordance with the new procedures set forth in 1017 OG 4, April 6, 1982.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS

*Carl I. Brundidge*  
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Carl I. Brundidge  
Registration No. 29,621

(202/828-0300)  
Attachments

*Approval  
#EWS*